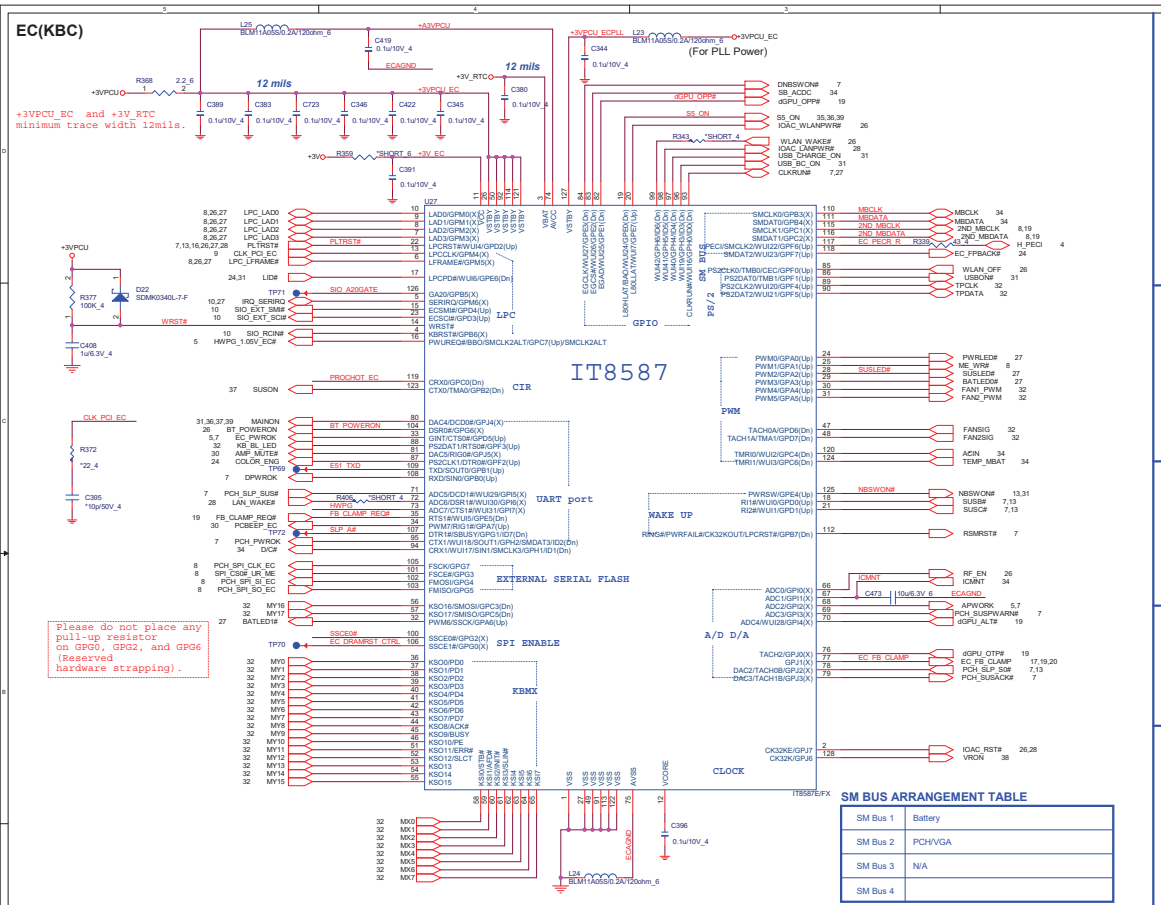
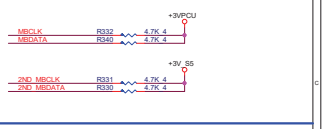
EC(KBC)

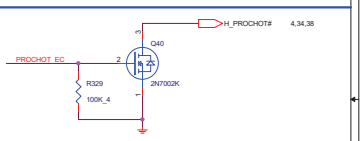


SM BUS ARRANGEMENT TABLE

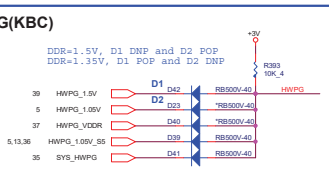
SM Bus 1	Battery
SM Bus 2	PCH/VGA
SM Bus 3	N/A
SM Bus 4	

SM BUS PU(KBC)

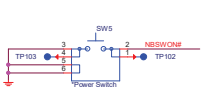




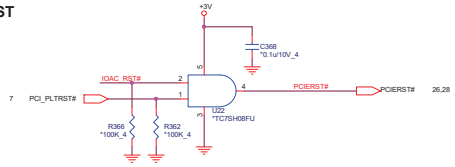
HWPG(KBC)



For test only



iRST



Quanta Computer Inc.	
PROJECT : ZRQ	
Size	Document Number: KBC IT8587
Rev	3A
Date: Friday, April 12, 2013	Sheet 33 of 47